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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of:

MUSTIN K. BRASK
MARK L. DOCZY
JACK KAVALIEROS
MATTHEW V. METZ
CHRIS E. BARNS
UDAY SHAH
SUMAN DATTA
CHRISTOPHER D. THOMAS
ROBERT S. CHAU

Serial No.: 10/748,559

Filed: December 29, 2003

For: A CMOS DEVICE WITH
METAL AND SILICIDE GATE
ELECTRODES AND A

METHOD FOR MAKING IT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Art Unit: 2811

Examiner: unknown

Attorney Docket: P18244

## INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the references cited on that form. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made

and is not to be construed as an admission that the information cited in this statement constitutes prior art or is otherwise material to patentability.

Respectfully submitted,

Dated: 9-24-2014

Mark V. Seeley Reg. No. 32,299

Intel Corporation Mail Stop SC4-202 2200 Mission College Blvd. Santa Clara, CA 95052-8119 (408) 765-7382

## FIRST CLASS CERTIFICATE OF MAILING

(37 C.F.R. § 1.8(a))

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